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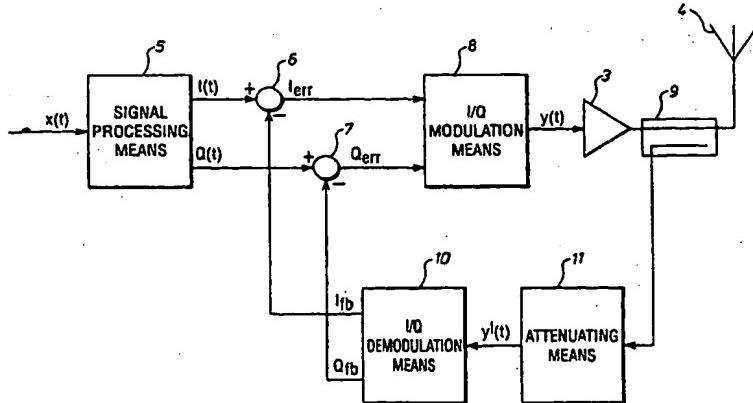
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(54) Title: SIGNAL PROCESSING SYSTEM



(57) Abstract

There is described a signal processing system comprising at least one control loop having a forward path and a feedback path; signal processing means (5) for producing a representation of the desired downstream output amplitude and phase as a pair of substantially orthogonal signal components ($I(t)$, $Q(t)$); feedback-path splitting means (10) for splitting a feedback signal ($y'(t)$) into a plurality of substantially orthogonal feedback signal components (I_{fb} , Q_{fb}); first combining means (6, 7) for combining the substantially orthogonal feedback signal components (I_{fb} , Q_{fb}) with the corresponding substantially orthogonal signal components ($I(t)$, $Q(t)$) to create error signals (I_{err} , Q_{err}); a plurality of modulating means (8d, 8e) which control the amplitude, frequency and/or phase of said pair of substantially orthogonal signal components ($I(t)$, $Q(t)$) on the basis of control signals (I'_{err} , Q'_{err}) which are derived from the error signals (I_{err} , Q_{err}); and second combining means (8a) for combining the outputs of said plurality of modulating means (8d, 8e) into an output signal ($y(t)$); wherein a phase-shift control means (12) in the forward path of the control loop which derives said control signals (I'_{err} , Q'_{err}) from said error signals (I_{err} , Q_{err}) on the basis of monitored signal values from at least one predetermined point (13-17) in the signal processing system.

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SIGNAL PROCESSING SYSTEM

The present invention relates to a signal processing system which uses Cartesian loop control. In particular, the present invention relates to a system which may be 5 used for linearising a power amplifier for use in, for example, a radio transmitter which, in turn, may be used in, for example, a VHF digital air traffic control communications system.

Many different signal processing systems which are 10 capable of producing output signals and controlling downstream components or systems in an accurate fashion are known in the art. One class of such signal processing systems is that of feedback control systems.

One particular method of feedback control is that 15 of Cartesian feedback control, wherein a control input is split into orthogonal components - an in-phase and a quadrature component - and a feedback signal is similarly split. Feedback is then carried out using these split control input and feedback signals.

20 A particular application for such Cartesian feedback control is in the linearisation of amplifier stages.

A particular application of linearised amplifier stages is in radio transmitters.

For ease of understanding, the present invention 25 will be described in terms of a particular embodiment in the area of radio transmitters, though its application to many and varied other areas of technology would be

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evident to a person skilled in the art.

Fig. 1 shows a schematic drawing of a conventional radio transmitter. A signal to be transmitted is created at block 1 - via a microphone, for example; this signal 5 is then processed in block 2 - modulated, converted to an appropriate frequency for transmission etc.; the processed signal is then amplified (3) and transmitted via some form of antenna (4).

In various applications, the purity of the spectrum 10 has to be very high. This is especially true for applications where a fixed waveband has been allocated for a particular use and there is a desire to get as many users into the available waveband as possible. Such applications include, for example: air traffic control, 15 mobile telephones, radio and television station broadcasts.

For certain modulation standards, the requirement for high spectral purity implies a need for an accurate linear amplification stage (3) in the transmitter. Three 20 options appear to suggest themselves for adequately linearising a transmitter power amplifier:

- (i) The use of a very large power amplifier running well below its maximum power and thus well away from non-linear regions. This is the simplest option, but is very 25 inefficient and is a relatively cumbersome option - inevitably large and expensive to make and run.
- (ii) The use of a feedforward control technique in

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which an error signal is formed from the output of the amplifier, by comparison with the input, and the error signal is fed forward and combined in a coupler with the amplifier output to reduce distortion. This technique
5 does allow the use of power-efficient amplifiers, but, it has associated problems. The circuitry of a feedforward technique circuit is sensitive to requirements for the matching of components in the various loops and in transmitter/receiver matching
10 requirements. Such circuits are thus dependent on component characteristics, such as temperature dependence etc. Thus there are practical limits to the degree of spectral control which can be achieved using this feedforward technique and there is substantial cost
15 involved in achieving adequate component matching.

(iii) The use of the Cartesian loop technique, which, in its conventional form (see, for example, IEE Conference Publication Number 235, pages 161 - 165; V. Petrovic: "VHF SSB Transmitter employing Cartesian Feedback"), allows the use of a power-efficient amplifier and provides very accurate spectral control without the particular matching requirements of the feedforward technique.

However, Cartesian loop control requires control of
25 phase shift around the loop, and this introduces problems with tuning the transmitter over a band of frequencies. In Cartesian controllers, the transmitter has to be

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readjusted each time the transmission frequency is changed. Prior art controllers used phase-shifting networks operating at radio frequency (RF). These RF phase-shift networks are difficult to design and limited
5 in their phase control range, frequently making it necessary to introduce phase control at more than one point in the circuit. Furthermore, they are subject to drift with time and temperature. If a transmitter is to cover anything other than an extremely narrow band of
10 frequencies, the RF phase-shifting networks need to be readjusted each time a frequency change is made. This is either done manually, which is time consuming and requires skilled personnel, or automatically controlled, e.g. by a microprocessor, which places further demands
15 on the RF phase-shifting network.

A conventional Cartesian feedback apparatus, such as that described in Petrovic's paper (*supra*), is shown in Fig. 2. In such conventional Cartesian feedback apparatus, the error signals (I_{err} , Q_{err}) are used
20 directly to control the magnitude of the I and Q components derived from the VCO (Voltage Controlled Oscillator) and thus control the magnitude and phase of the transmitter output.

Conventional Cartesian feedback methods use a single
25 VCO for "converting up" signals to radio frequency (RF) as shown in Fig. 3 (see also Fig. 3 of IEE Conference Publication Number 235, pages 161 - 165; V. Petrovic:

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"VHF SSB Transmitter employing Cartesian Feedback").

WO-A-96/37948 (British Technology Group) discloses a method for producing a linear transmitter. In this method, the output of the circuit is provided by the 5 control of the two separate oscillators such that their sum is the desired output signal. These separate VCOs will need to be accurately matched and thus such a system will be incapable of the desired accuracy unless a Cartesian feedback is also used. Such a system does not 10 appear to effectively overcome the limitation of not being able to produce a Cartesian feedback loop system which can operate over a broad band of operating frequencies.

An object of the present invention is to provide a 15 signal processing system and method which very accurately control a downstream component or system over a wide band of frequencies, using a feedback technique which has the advantages of Cartesian loop control mentioned above and which does not have the difficult component-matching 20 requirements of some of the techniques mentioned above.

In one aspect of the invention, such a system is provided by a signal processing system which comprises:

at least one control loop having a forward path and a feedback path;

25 signal processing means for producing a representation of the desired downstream output amplitude and phase as a pair of substantially orthogonal signal

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components;

feedback-path splitting means for splitting a feedback signal into a plurality of substantially orthogonal feedback signal components;

5 first combining means for combining the substantially orthogonal feedback signal components with the corresponding substantially orthogonal signal components to create error signals;

10 a plurality of modulating means which control the amplitude, frequency and/or phase of the pair of substantially orthogonal signal components on the basis of control signals which are derived from the error signals; and

15 second combining means for combining the outputs of said plurality of modulating means into an output signal;

wherein, said signal processing system further comprises a phase-shift control means in the forward path of the control loop which derives said control signals from said error signals on the basis of monitored signal 20 values from at least one predetermined point in the signal processing system.

Such a system has the advantages that it provides the very high accuracy of Cartesian feedback control for the control of downline components or systems whilst being able to operate over a wide band of operating frequencies and that it does not have the difficult component-matching requirements or transmitter/receiver

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matching requirements of some other systems. This latter advantage, in turn, means that the system of the invention is easily repeatable - it may be produced in large quantities without the need for extended set-up and
5 trimming procedures which are both time-consuming and costly. Using such a set-up, the phase may be adjusted at will - anywhere between 0° and 360°.

Some other aspects and advantages of the invention are as follows:

10 the second phase-shift control means may operate at baseband frequency - allowing the use of simple, repeatable and cheap components;

the phase-shift control means may be digital;

15 the phase-shift control means may be digitally controlled;

the phase-shift control means may be auto-calibrated. This could be achieved in situations where an initialising input signal sequence (a "preamble") is known and the phase-shift control means can check what
20 the feedback is for this known sequence, then, if the feedback is wrong, it can be automatically trimmed accordingly;

if the phase-shift control means is digitally controlled, it may be made self-calibrating using the
25 same principle as for auto-calibration and running a simple algorithm in the digital control means in order to self-trim the phase-shift control means.

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The phase-shift control means can also be used to control the gain of the control loop and thus be used to compensate for variable gains within other loop components.

5 Further aspects, advantages and objectives of the invention will become apparent from a consideration of the drawings and the ensuing description. In the drawings:

10 Fig. 1 is a schematic drawings of a conventional radio transmitter.

Fig. 2 is a schematic drawing of a signal processing system using conventional Cartesian feedback control.

15 Fig. 3 is a schematic depiction of a prior art up-conversion and combination means as conventionally used in Cartesian feedback control.

Fig. 4 is a schematic depiction of the signal processing system of this invention in use controlling a power amplifier.

20 Fig. 5 is a more detailed system diagram for a radio transmitter circuit which uses the signal processing system of this invention.

Fig. 6 is a detailed system diagram for a radio transceiver which uses the signal processing system of this invention.

25 Referring now to the drawings, Fig. 4 is a schematic depiction of the signal processing system of this invention in use controlling a power amplifier. In this

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embodiment, a signal which is to be transmitted ($x(t)$) is split into an in-phase and a quadrature component ($I(t)$ and $Q(t)$) by a forward-path splitting means. In-phase and quadrature feedback components (I_{fb} and Q_{fb}) are then subtracted from the input signal components to form error signals (I_{err} and Q_{err}). This subtraction is done by comparators 6 and 7. The resultant error signals are then passed through phase-shift control means (12) to an I-Q modulation means (8) which modulates and combines the in-phase and quadrature error signals (I_{err} and Q_{err}) to form a pre-amplification output signal $y(t)$. This pre-amplification output signal then passes to the power amplifier (PA) (3) and subsequently to an antenna means (4) for transmission.

The amplified output signal passes through a coupler (9) from which this signal is fed back through an attenuator (11). The attenuated feedback signal ($y'(t)$) is split into in-phase and quadrature feedback components (I_{fb} and Q_{fb}) by an I-Q demodulation means (10) and these feedback components are then combined with the input signal components ($I(t)$ and $Q(t)$) as described above.

The phase-shift control means (12) is digitally controlled and has monitor inputs connected to various points of the circuit. In the particular embodiment of Fig. 5, the monitor inputs are from the transmission signal at terminal 13, from the in-phase and quadrature feedback components at terminals 14 and 15 and from the

-10-

in-phase and quadrature error signals at terminals 16 and 17, though additional monitor inputs could also be taken or a selection from these inputs may be sufficient in other circumstances. Thus the phase-shift control means 5 (12) accurately controls the phase shift of the signals based directly on the monitor inputs.

Fig. 5 shows a particular embodiment of the invention in a radio transmitter circuit.

The present invention is embodied in the PA control 10 module 18, which is labelled "PA Control Module". The numerals, as used in preceding paragraphs, represent the same system parts and a repeated description of these parts is omitted.

In Fig. 5, the frequency range f_4 to f_5 is the 15 desired range of radio transmission frequencies which are to be produced by the transmitter.

An oscillator (19) is connected to the I-Q modulation and demodulation means (8, 10) via a signal splitter (20), in order to up-convert/down-convert the 20 incoming signals to an intermediate frequency (IF), f_1 - intermediate between baseband and the particular desired transmission frequency. The signal in the forward path passes, via a buffer (24) to an up-conversion means (22) which up-converts the signal to a higher, radio frequency 25 (RF), between f_2 and f_3 , which is close to the desired transmission frequency (ie. close to the desired

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frequency which is in the range of f_4 to f_5). This up-conversion from IF to RF is carried out using a synthesizer circuit (24) which is connected to the up-conversion means (22) via a signal splitter (23). The 5 synthesizer is controlled to produce a signal at the desired frequency within the wide band of frequencies between f_2 and f_3 .

The outgoing signal is then passed through a pair of tunable filters (25, 26) which are separated by a 10 buffer circuit (26). These filters are controlled by a processor (28) in order to further increase the spectral accuracy of the radio transmitter. In a similar manner to the phase control, this frequency control is based on monitored signal values from at least one predetermined 15 point (13-17) in the signal processing system.

The output of the second tunable filter (27) is output to the downline circuit which is to be controlled, in this case a power amplifier (3).

The output of the downline circuit which is to be 20 controlled, in this case the power amplifier (3), is sensed at a coupler (9) and is appropriately attenuated by an attenuator (11). This feedback signal is then down-converted to an intermediate frequency (IF) by a down-conversion means (21) which is connected via a 25 signal splitter (23) to the synthesizer module (24). This IF feedback signal is then down-converted to baseband and split into two substantially orthogonal

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feedback signals (I_{fb} , Q_{fb}) by the I-Q demodulation means (10) which is connected to an oscillator (19) via signal splitting means (20).

The two substantially orthogonal feedback signals 5 (I_{fb} , Q_{fb}) are fed back, via two buffer circuits (29, 30) to differencing means (6, 7) where they are combined with the input signal components ($I(t)$, $Q(t)$) and subsequently integrated by integration circuits (31, 32) to form the error signals (I_{err} , Q_{err}).

10 In this particular embodiment, the splitting of the input signal ($x(t)$) into substantially orthogonal components ($I(t)$, $Q(t)$) is carried out by a Digital Signal Processing (DSP) Module (5).

15 The input signal ($x(t)$) is converted into digital form by an analogue-digital converter (44), then appropriately split by a digital signal processor (39) and subsequently, the two substantially orthogonal signal components are converted back into analogue form by digital-analogue converters (42, 43).

20 The control loop of the second phase-shift control means (12) is closed via a multiplexer (41), and analogue-digital converter (40), a digital signal processor (39) and a slave processor (28). This same control loop is used to control the tunable filters (25, 25 27) at the output stage of the PA control module (18).

A final bandpass filter (53) is provided after the feedback coupler (9), for effecting elimination of

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unwanted transmitter harmonics.

The transmitter formatter module (54) allows the radio transmitter system to be used for sending voice data which may be input from a microphone (not shown) 5 which would be plugged into a microphone socket (48) of the front panel (33, 55) of the radio transmitter or for sending data from other sources, ie. any sort of data, including voice data or computer data. Non-voice data or remote voice data may be input via a connector (52).
10 Such data is then appropriately formatted by the transmitter formatter Module (54) and passed on to the transmitter DSP module (5) for splitting into substantially orthogonal signal components ($I(t)$, $Q(t)$).

Various circuits (34, 35) are also provided for 15 controlling the bias of the power amplifier (3), based on its operating temperature. A variety of monitoring, controlling and failsafe circuits are also provided (36, 37, 38).

The radio transmitter circuit of Fig. 5 may be used 20 with a separate receiver circuit or it may be combined with a receiver circuit in order to form a transceiver circuit such as that in Fig. 7.

It is to be noted, in the circuit of Fig. 6, that the circuit only uses two DSP chips (39, 55). In prior 25 art transceiver circuits, transmission and reception have been separate activities and have thus required separate circuits. Prior art transceiver circuits required the

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use of one DSP chip for transmission/modulation and two DSP chips for reception/demodulation. These DSP chips are expensive. The transceiver of Fig. 6 uses only two chips and thus gives great advantages over prior art
5 circuits. This is achieved through the realisation (which would not be obvious to the person skilled in the art) that the processing power of the DSP chips may be shared between transmission and reception except for in the case of "loopback mode" operation. "Loopback mode" 10 operation is operation where the receiver side of the transceiver is used to monitor the output of the transmitter side. However, this loopback mode may be realised with a very simplified reception function, since the signal being monitored is strong, with a high signal- 15 to-noise ratio and the direction which the signals are coming from is known.

So, in the transceiver of Fig. 6 part of the two DSP chips (39, 55) is configured to operate as a receiver; part of the two DSP chips (39, 55) is configured to run 20 as a transmitter and part of the chips (39, 55) is configured to run as a very much simplified receiver - using a reduced sample rate. This innovative use of the chips allows the transceiver to be built using only two DSP chips with a corresponding cost reduction in 25 manufacture.

Although the invention has been described in terms of a radio transmitter/transceiver embodiment, the person

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skilled in the art would understand that the teaching of this invention can be easily applied to a large number of applications where accurate control of an element is required. The present invention presents a method of 5 improving Cartesian loop control systems which allows the use of a Cartesian loop in a system operating over a wide band of frequencies as a stable means to implement phase correction.

CLAIMS:

1. A signal processing system which comprises:
 - at least one control loop having a forward path and a feedback path;
 - 5 signal processing means (5) for producing a representation of the desired downstream output amplitude and phase as a pair of substantially orthogonal signal components ($I(t)$, $Q(t)$);
 - 10 feedback-path splitting means (10) for splitting a feedback signal ($y'(t)$) into a plurality of substantially orthogonal feedback signal components (I_{fb} , Q_{fb});
 - 15 first combining means (6, 7) for combining the substantially orthogonal feedback signal components (I_{fb} , Q_{fb}) with the corresponding substantially orthogonal signal components ($I(t)$, $Q(t)$) to create error signals (I_{err} , Q_{err});
 - 20 a plurality of modulating means (8d, 8e) which control the amplitude, frequency and/or phase of said pair of substantially orthogonal signal components ($I(t)$, $Q(t)$) on the basis of control signals (I'_{err} , Q'_{err}) which are derived from the error signals (I_{err} , Q_{err});
 - 25 and
 - second combining means (8a) for combining the outputs of said plurality of modulating means (8d, 8e) into an output signal ($y(t)$);
 - characterised in that said signal processing system further comprises:

a phase-shift control means (12) in the forward path of the control loop which derives said control signals (I'_{err} , Q'_{err}) from said error signals (I_{err} , Q_{err}) on the basis of monitored signal values from at least one 5 predetermined point (13-17) in the signal processing system.

2. A signal processing system according to claim 1, wherein the second phase-shift control means operates at 10 baseband frequency.

3. A signal processing system according to claim 1 or claim 2, wherein the phase-shift control means is digital.

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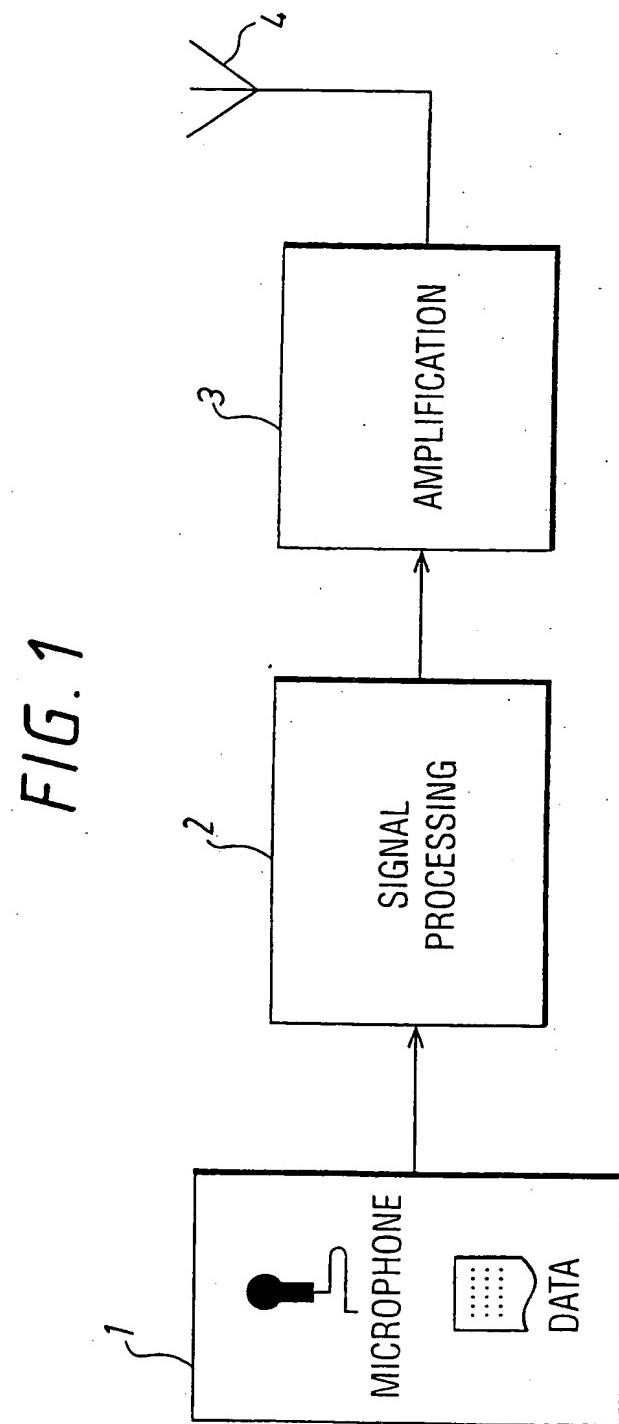
4. A signal processing system according to any preceding claim wherein the phase-shift control means is digitally controlled.

20 5. A signal processing system according to any preceding claim wherein the phase shift control means is auto-calibrated.

25 6. A signal processing system according to claim 5, in which self-calibration is achieved by using an initialising input sequence to generate a feedback signal and the phase-shift control means compares the feedback

signal generated with a predicted feedback signal and corrects the phase shift on the basis of the comparison result.

- 5 7. A signal processing system according to claim 6, wherein the phase-shift control means is digitally controlled, and wherein the digital control means effects correction on the basis of an algorithm.
- 10 8. A signal processing system according to any preceding claim, wherein the phase-shift control means controls the gain of the control loop and compensates for variable gains in other components in the loop.



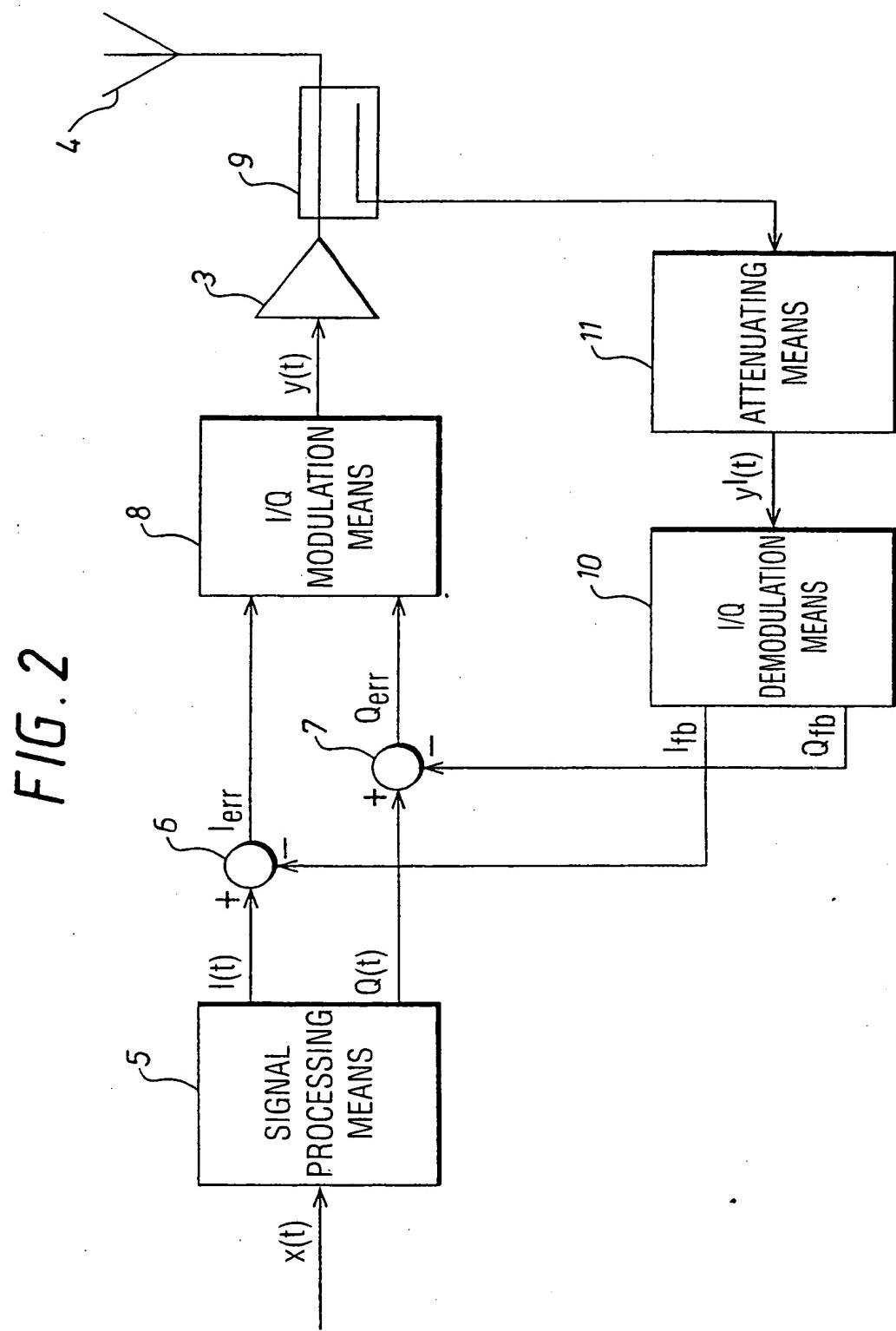
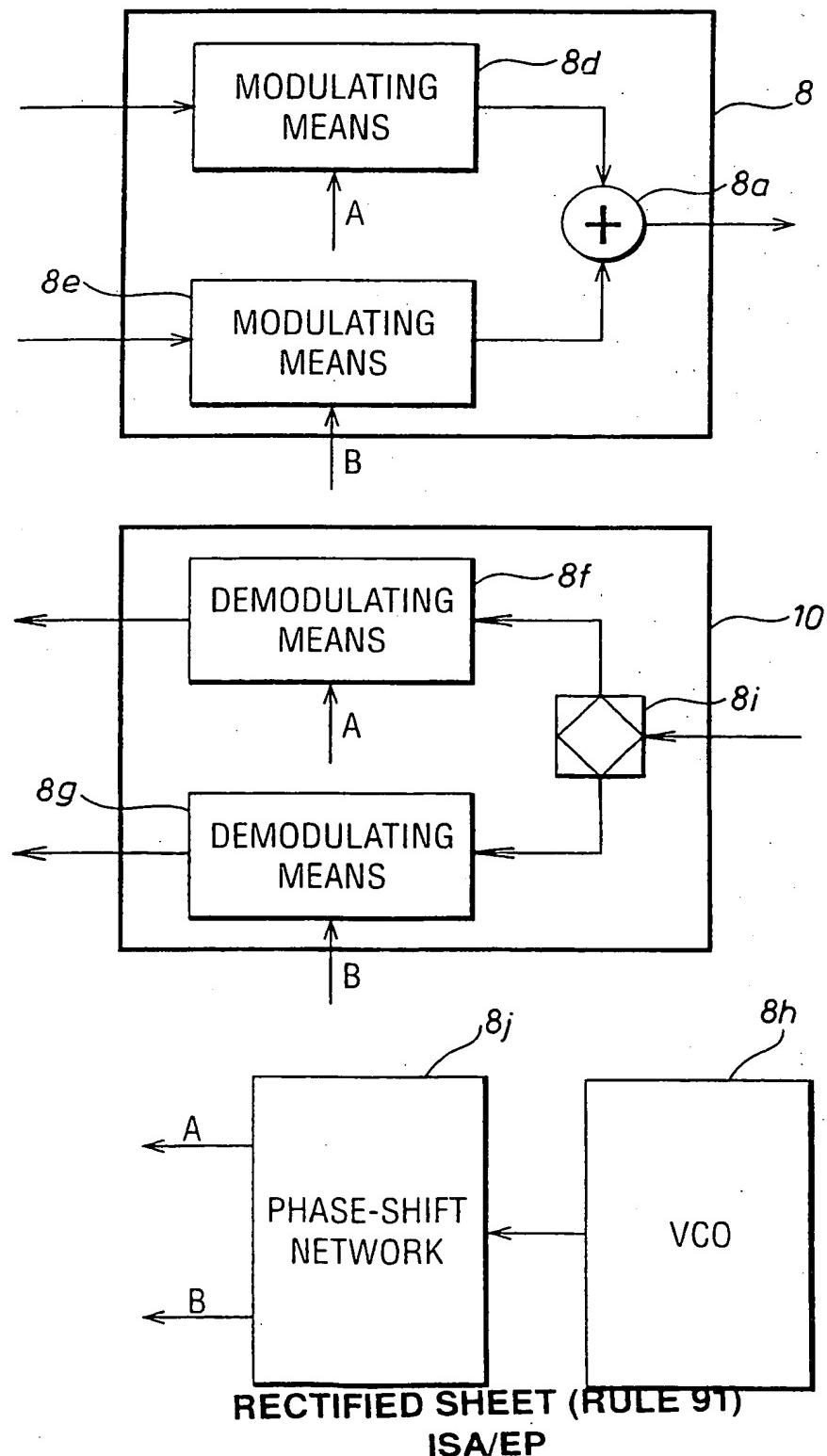
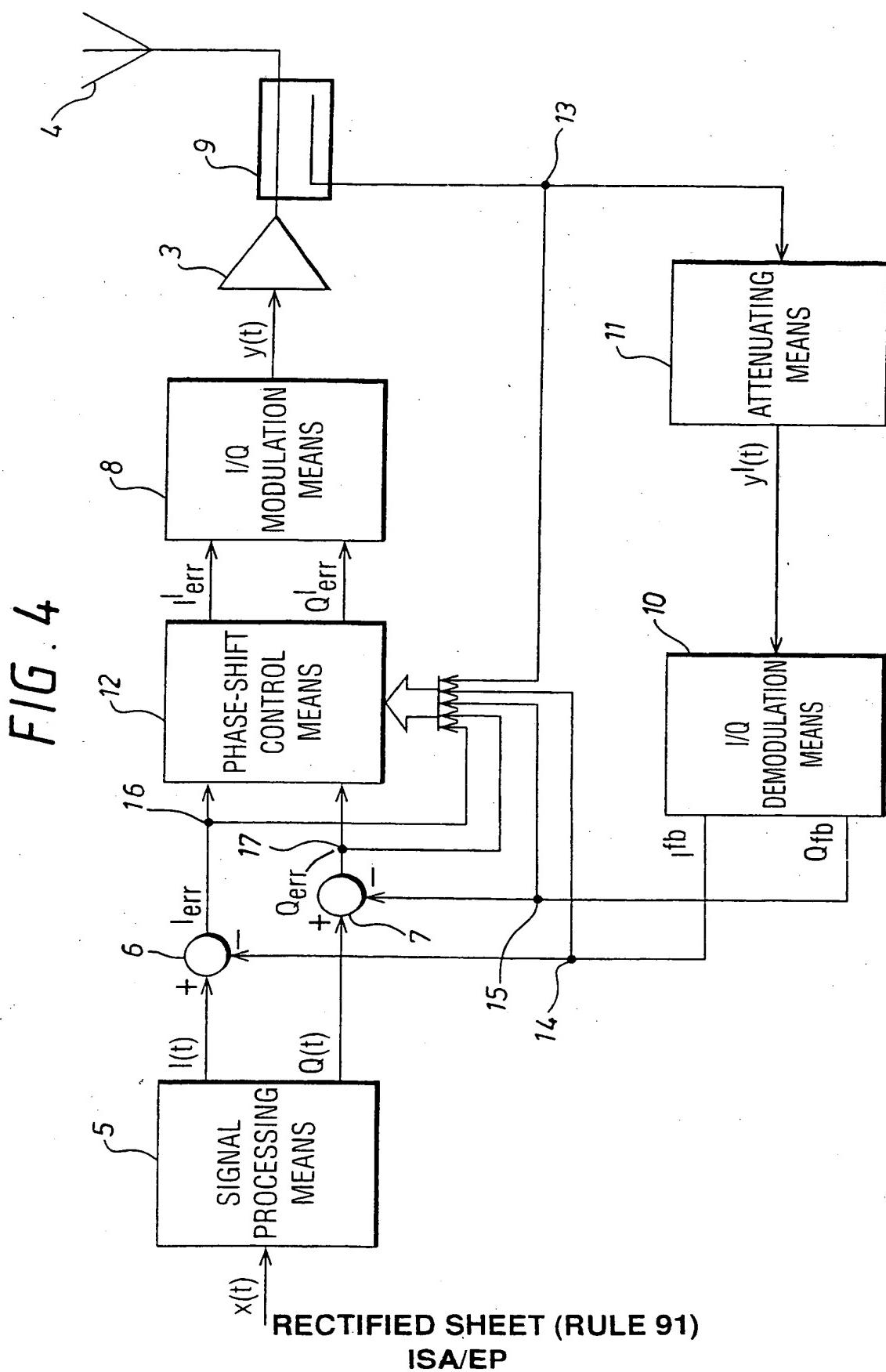


FIG. 3





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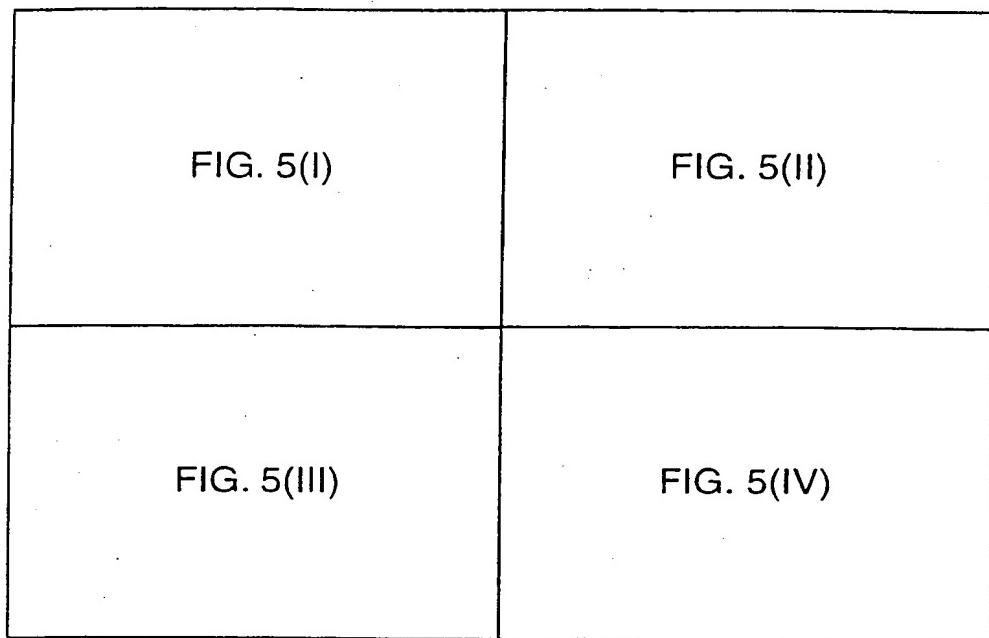
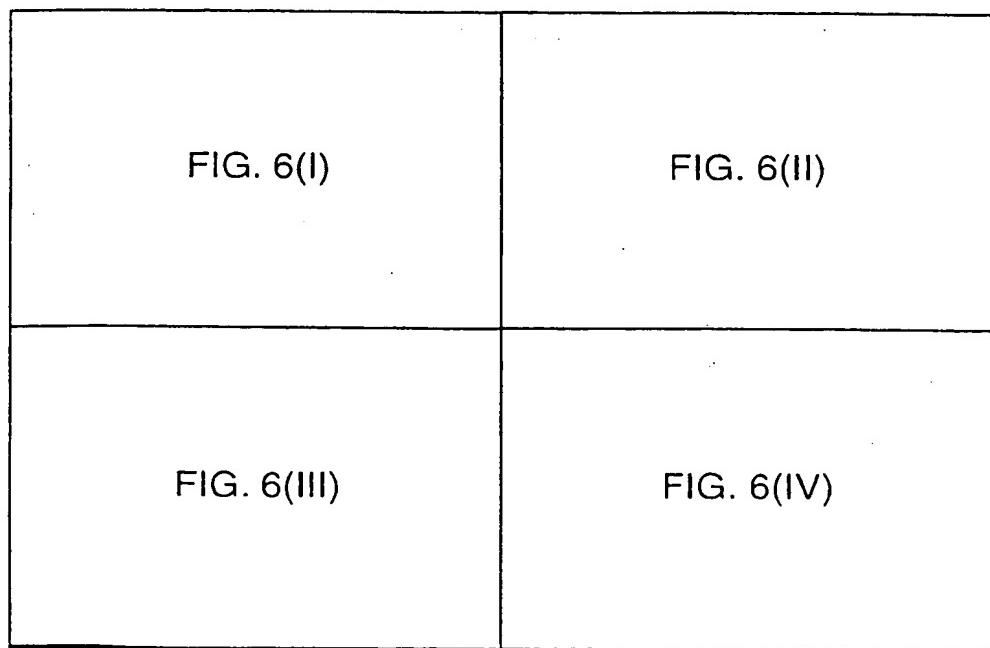
FIG. 5*FIG. 6*

FIG. 5(1)

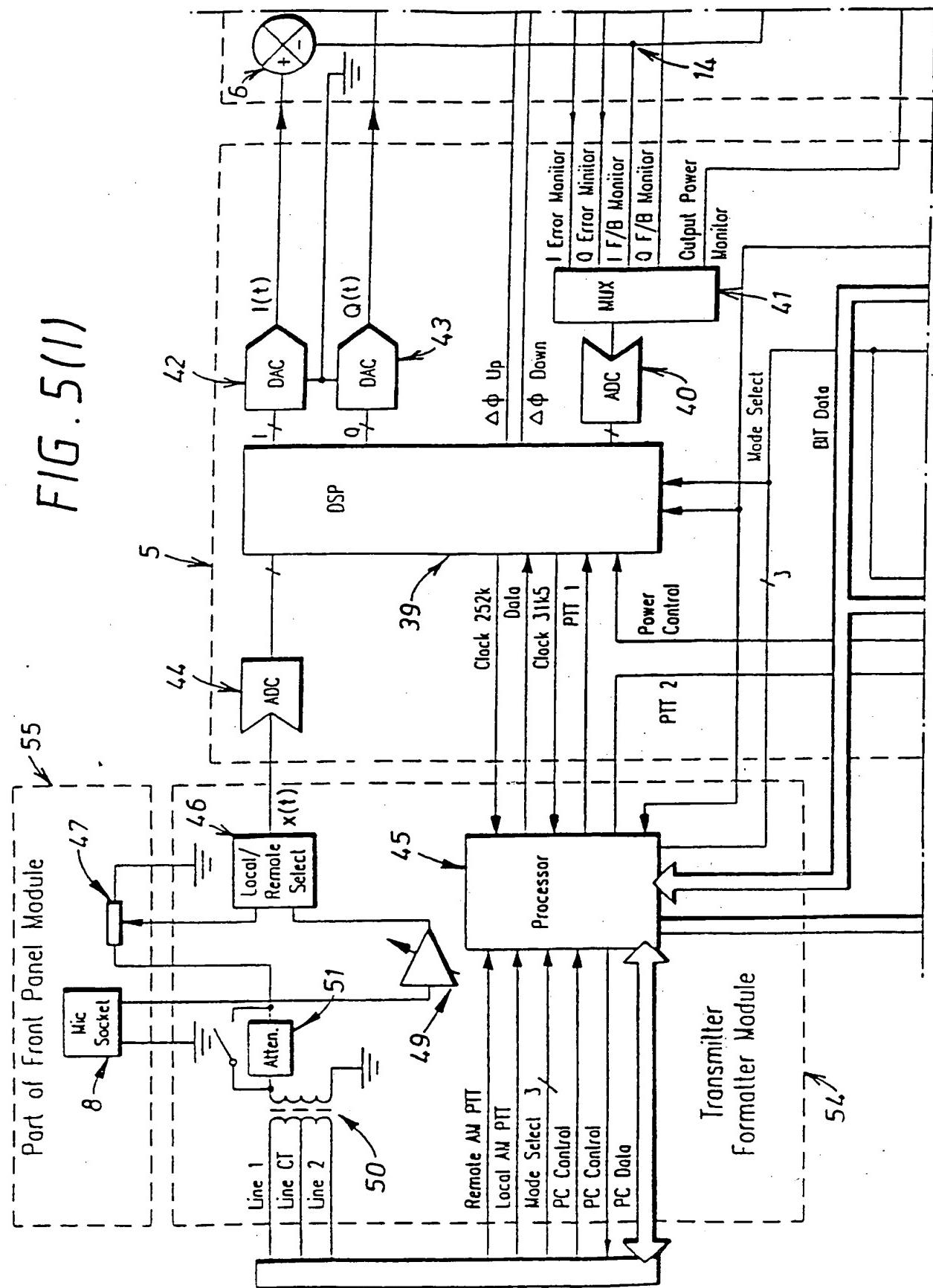
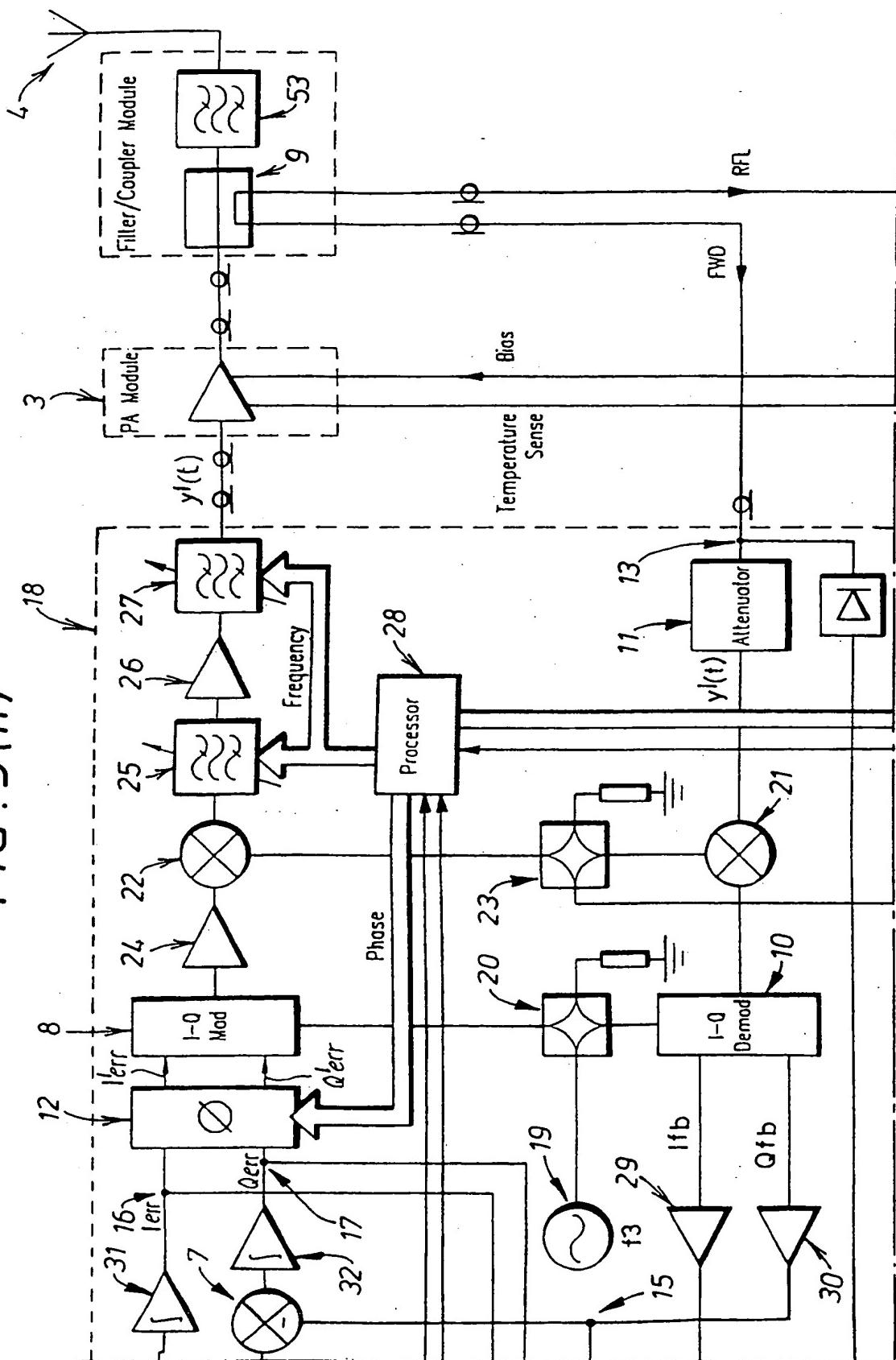


FIG. 5(III)



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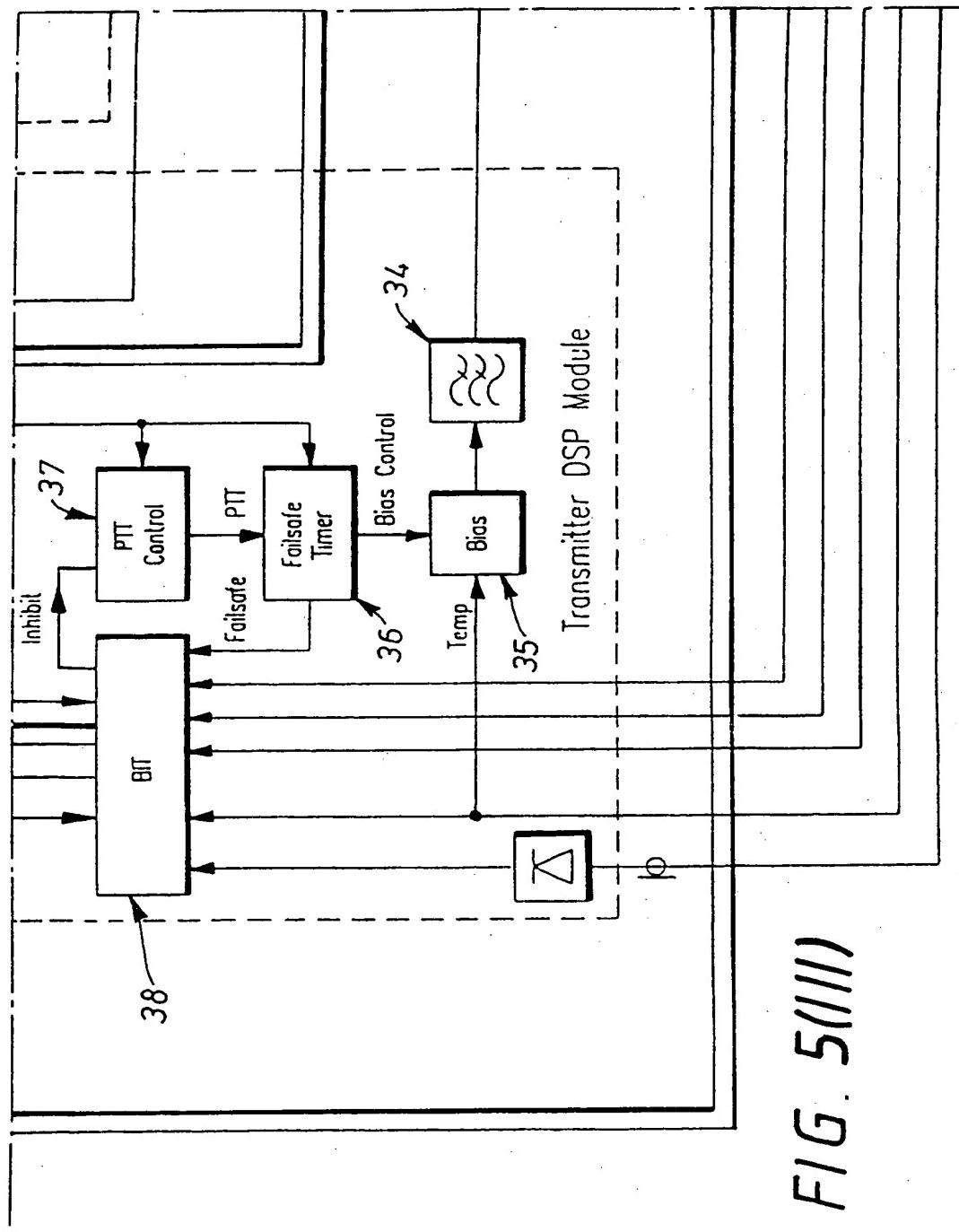


FIG. 5(1/11)

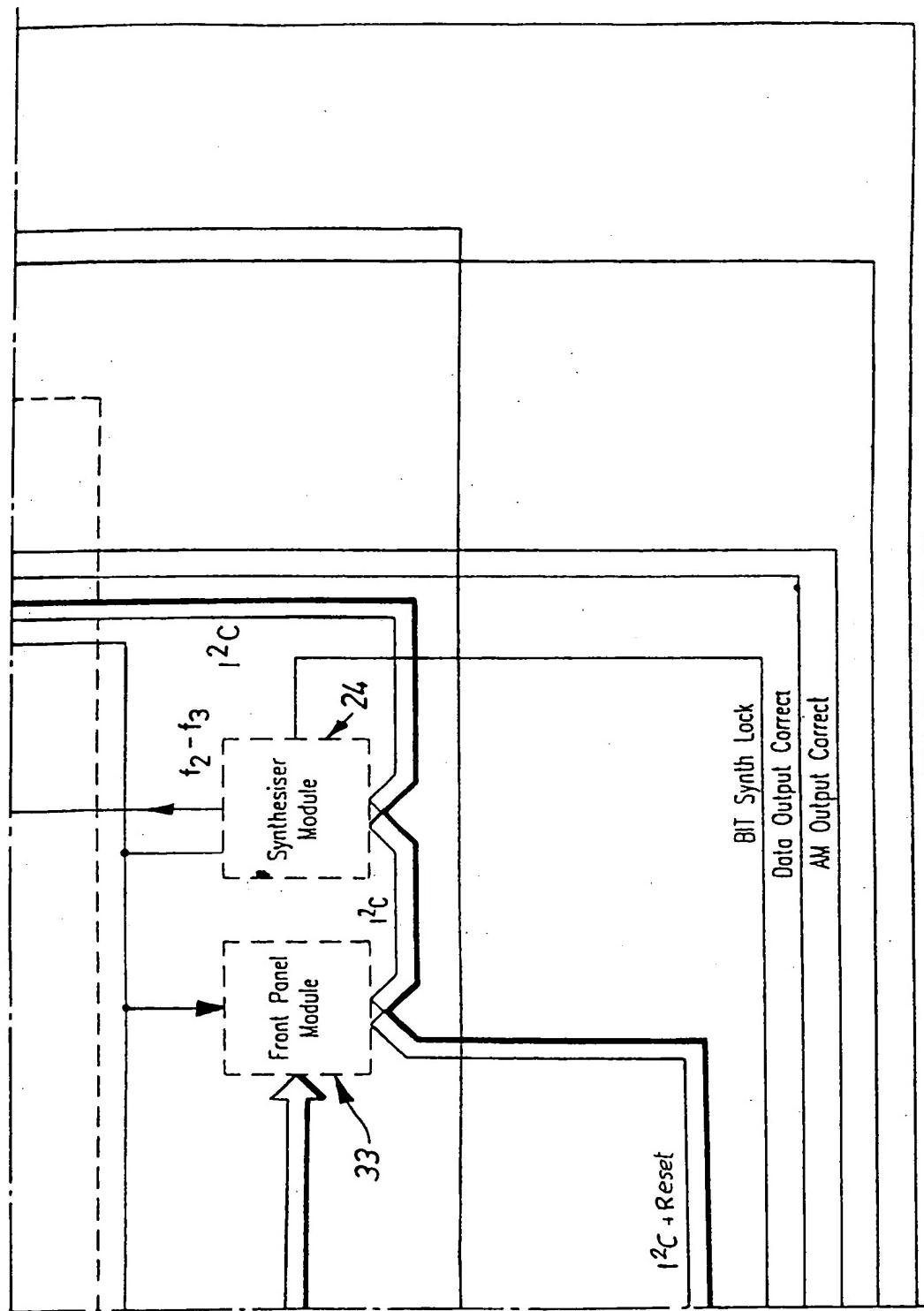


FIG. 5 (IV)

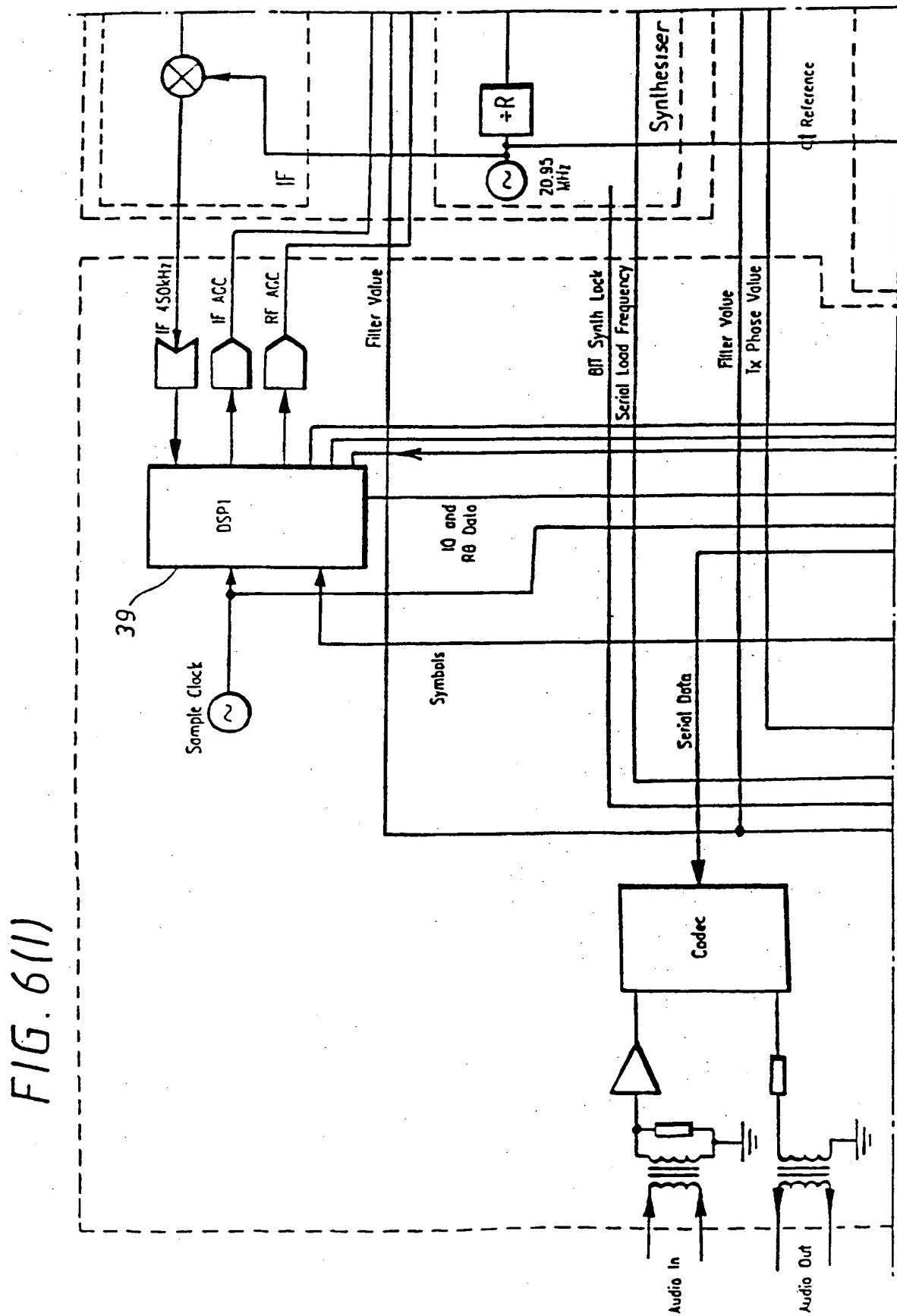
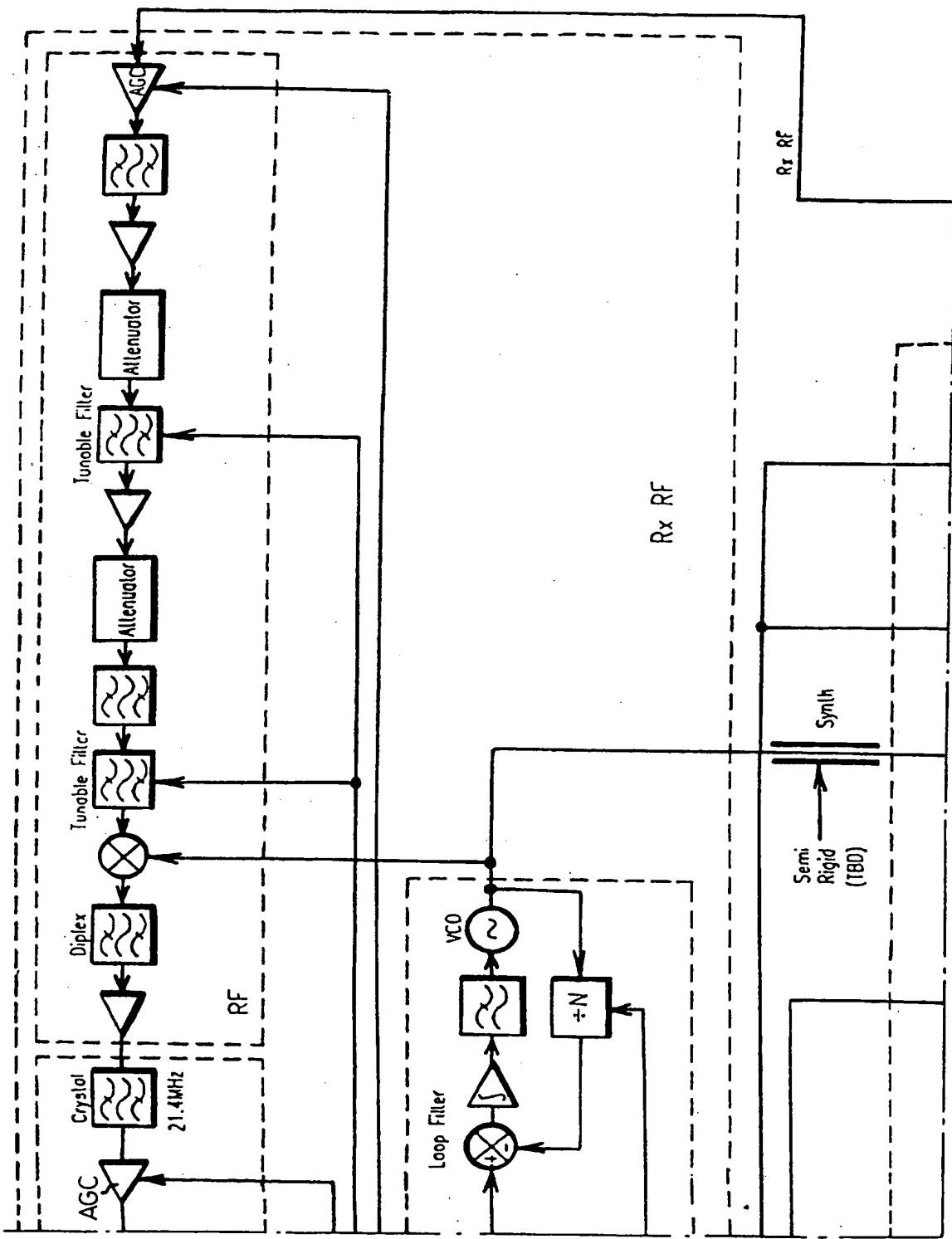


FIG. 6(1)

FIG. 6 (III)



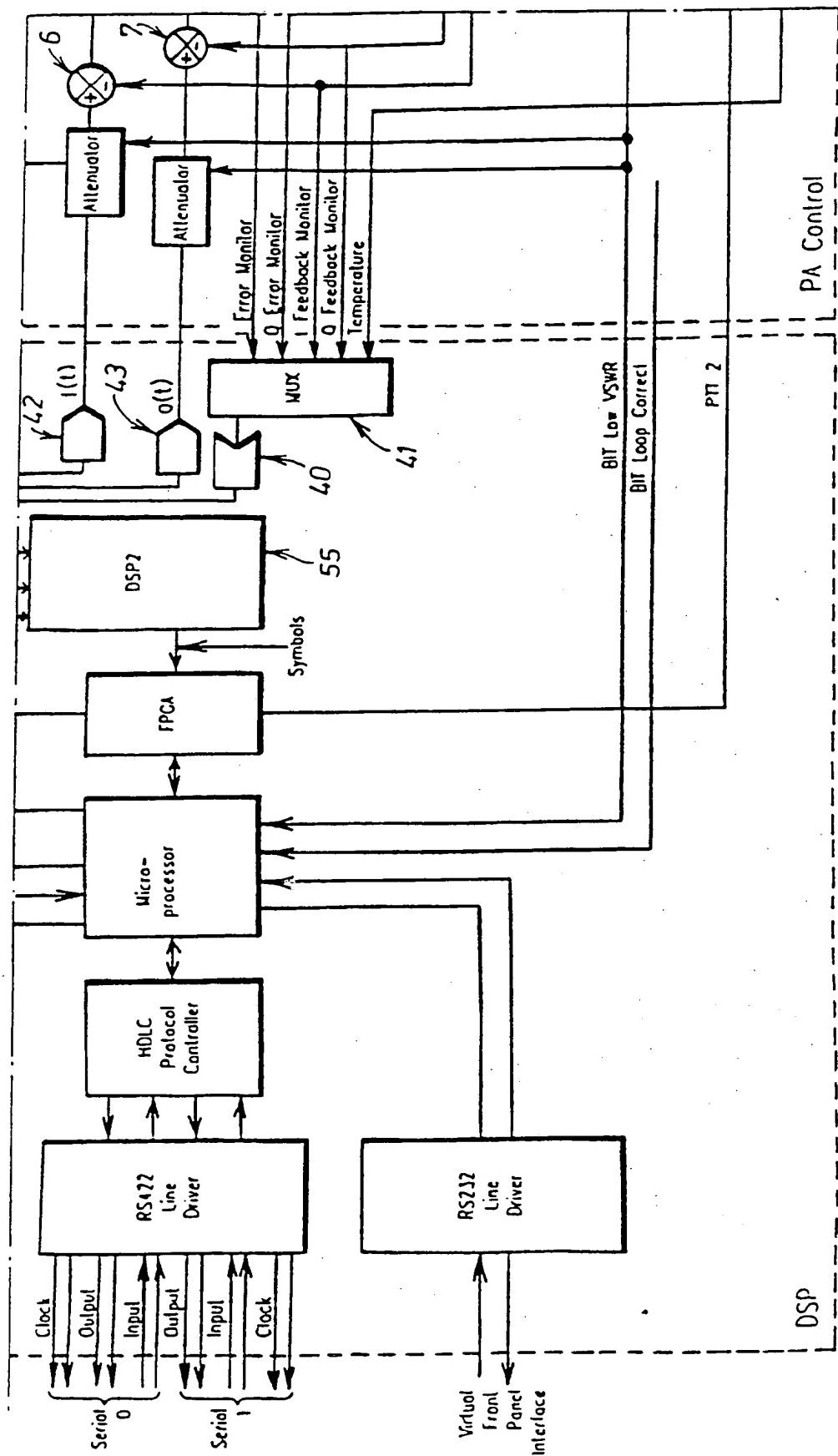
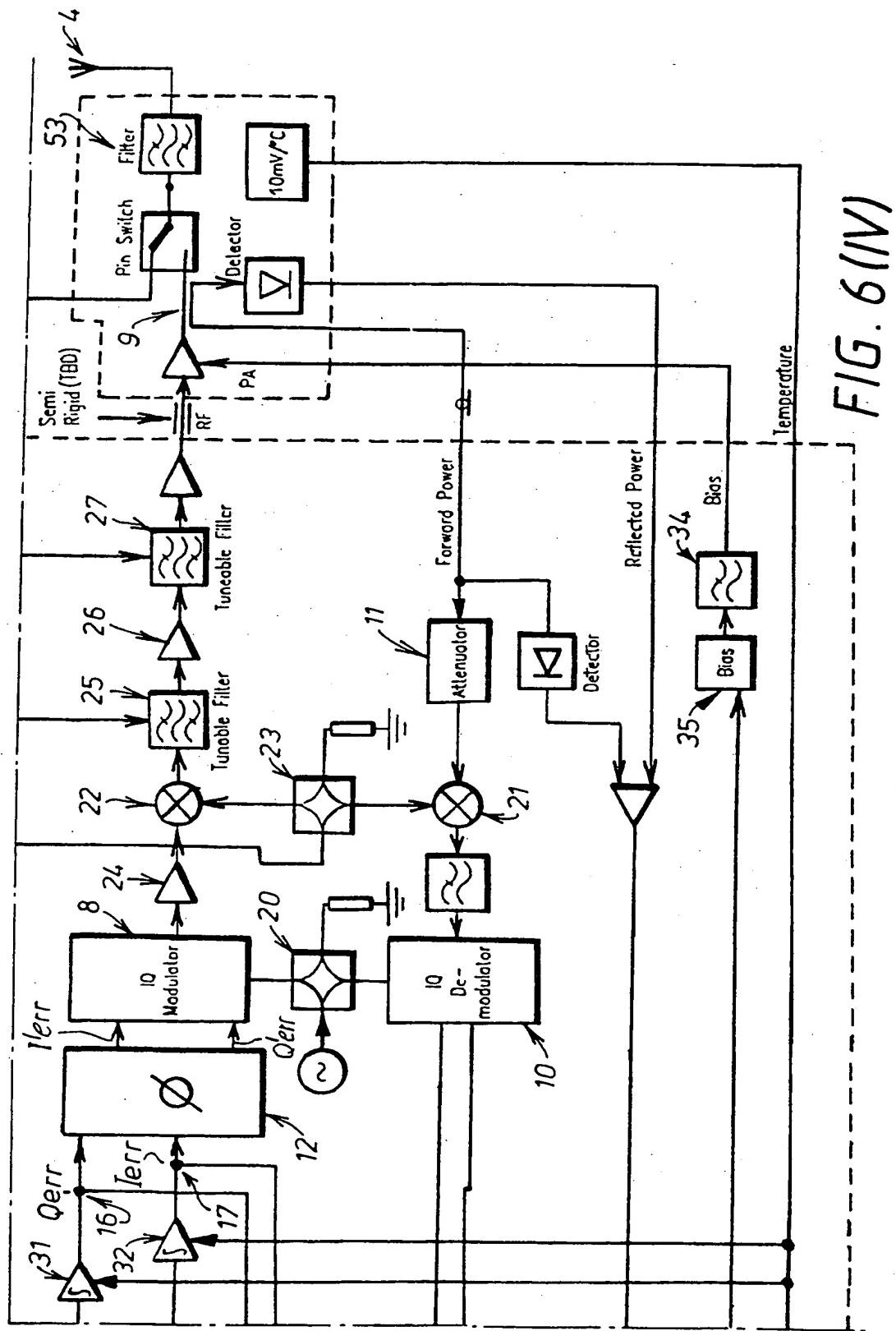


FIG. 6(111)



INTERNATIONAL SEARCH REPORT

International Application No

PCT/GB 98/02099

A. CLASSIFICATION OF SUBJECT MATTER

IPC 6 H03F1/32

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 6 H03F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	MANSELL A R ET AL: "TRANSMITTER LINEARISATION USING COMPOSITE MODULATION FEEDBACK" ELECTRONICS LETTERS, vol. 32, no. 23, 7 November 1996, page 2120/2121 XP000643852 see page 2120 - page 2121 --- US 5 574 992 A (CYGAN LAWRENCE F ET AL) 12 November 1996 see column 2, line 15 - column 3, line 65; figures 1,2 --- US 4 462 001 A (GIRARD HENRI) 24 July 1984 see abstract; figure 1 --- -/--	1-8
Y		1-8
Y		1-8

Further documents are listed in the continuation of box C.

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30 October 1998

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INTERNATIONAL SEARCH REPORT

International Application No
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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT		
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Information on patent family members

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